

Substitute for form 1449/PTO				Complete if Known	
FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/727,430
				Filing Date	December 4, 2003
				First Named Inventor	Mark L. Buer
				Art Unit	2134
				Examiner Name	POWERS, William S.
Sheet	1	of	1	Attorney Docket Number	2875.0240001

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)	MM-DD-YYYY		
/W.P./	US1	6,963,946 B1	11-08-2005	Dwork et al.	
	US2	7,003,118 B1	02-21-2006	Yang et al.	
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	US6	2003/0126233 A1	07-03-2003	Bryers et al.	
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FOREIGN PATENT DOCUMENTS					
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		Country Code ² Number ³ Kind Code ⁴ (If Known)	MM-DD-YYYY		
	FP1				
	FP2				
	FP3				
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	FP5				
	FP6				
	FP7				
	FP8				
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
/W.P./	NPL1	Pidgeon, Nick, "How Ethernet Works", 4/2003, www.howstuffworks.com , accessed via web.archive.org on 6/24/2007, pgs. 1,2.			
/W.P./	NPL2	Stevens, Richard, TCP/IP Illustrated, Vol. 1, 1994, Addison Wesley, pg. 21-23.			
/W.P./	NPL3	Cassidy, Kyle, "An Ethernet Primer", 2001 Que Publishing, pg. 1-8.			
/W.P./	NPL4	Krishna et al., "Distributed Processing in a Cryptography Acceleration Chip", U.S. Application No. 09/610,798 filed on July 6, 2000.			
	NPL5				
	NPL6				
	NPL7				
	NPL8				
	NPL9				
	NPL10				

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